

FIG. 1

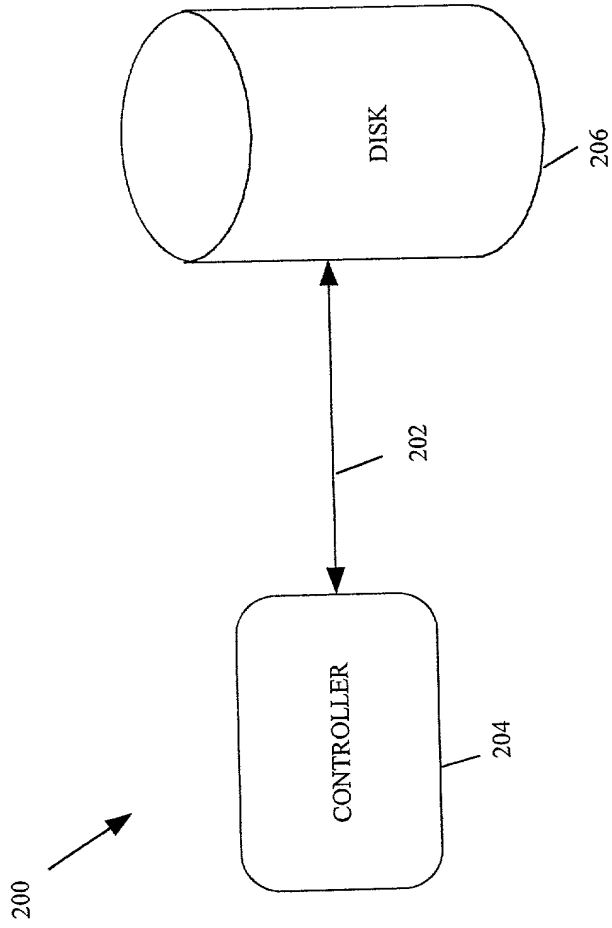


FIG. 2

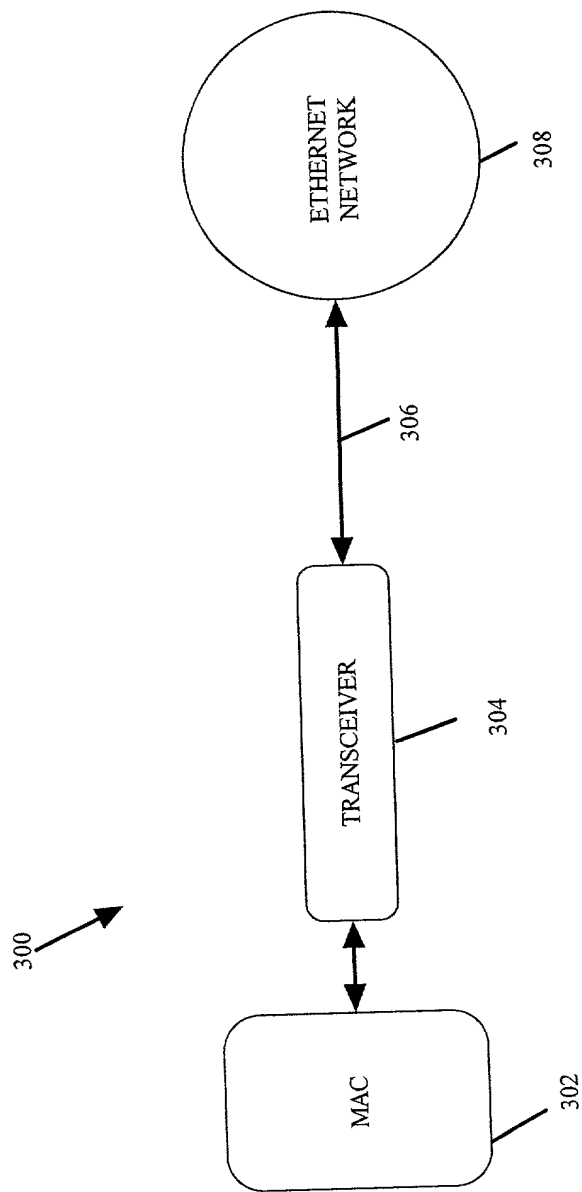


FIG. 3

400

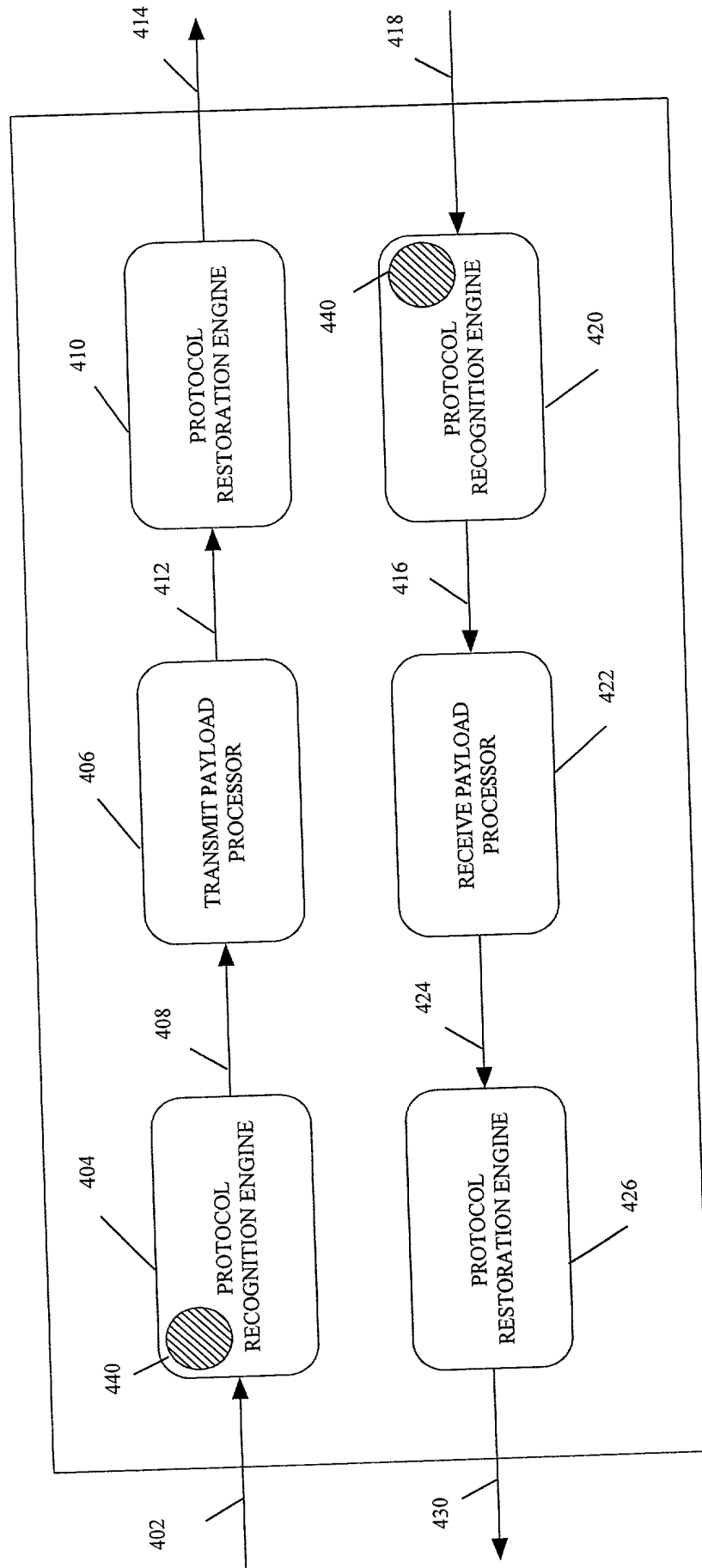


FIG. 4

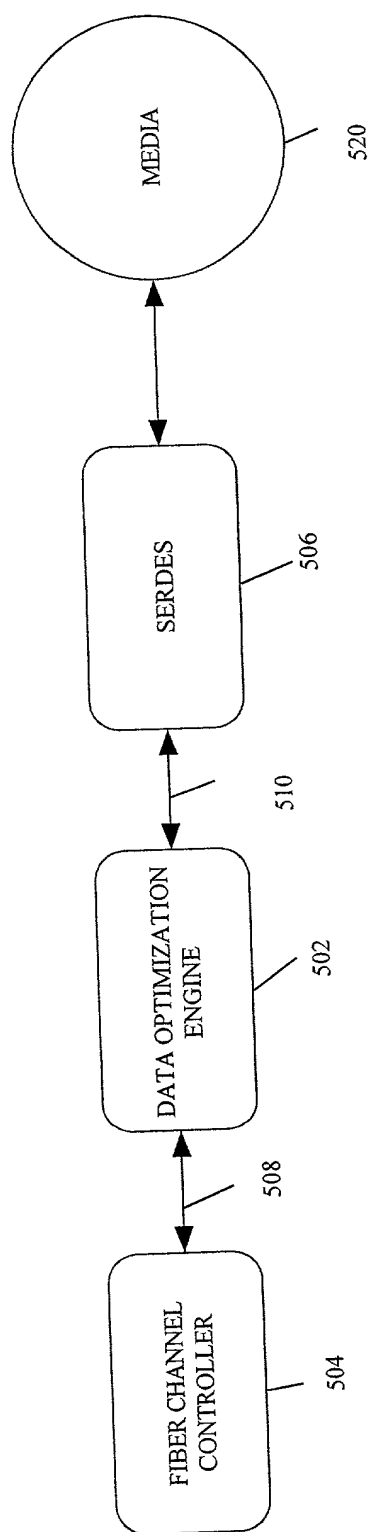


FIG. 5

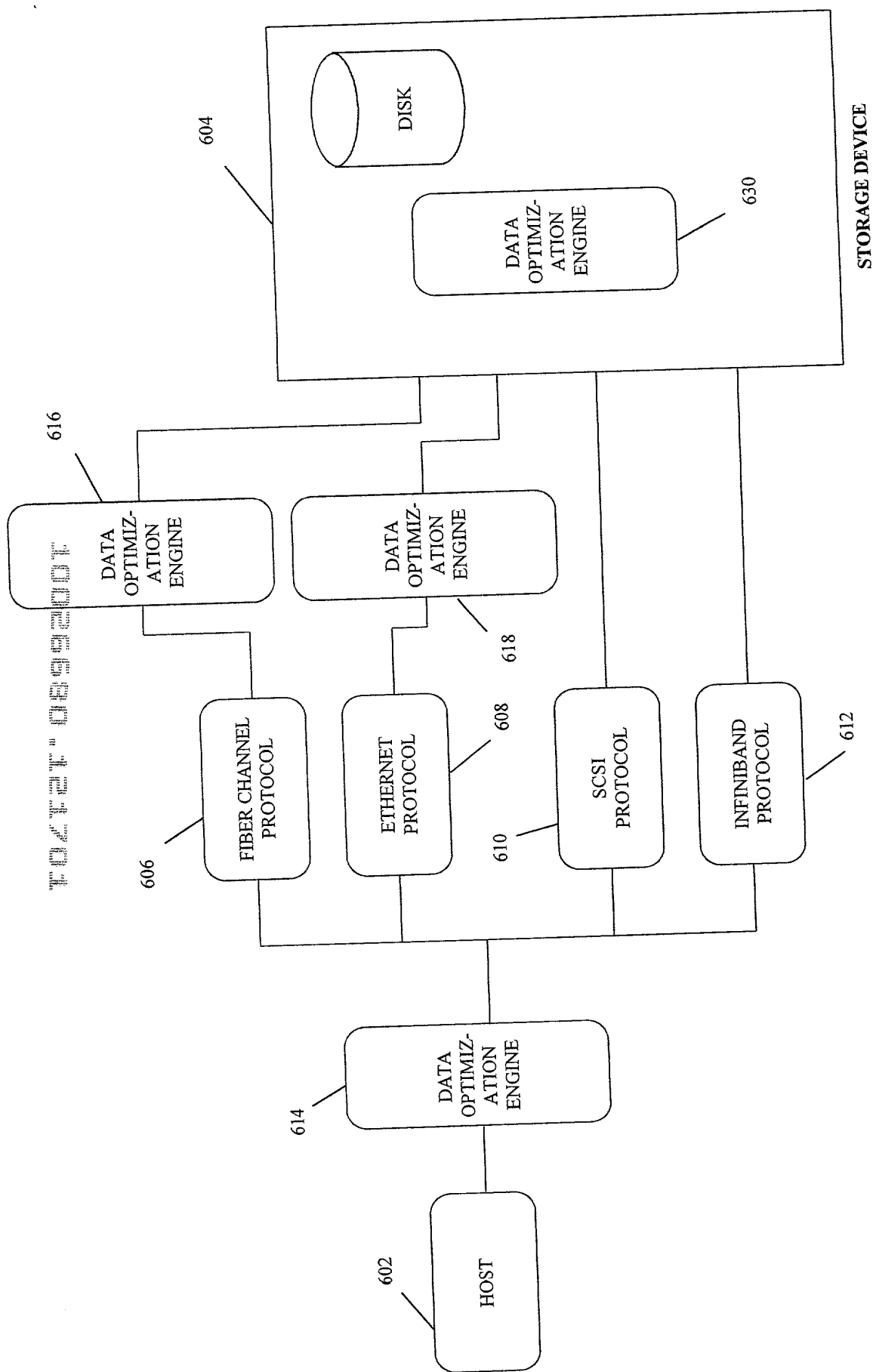
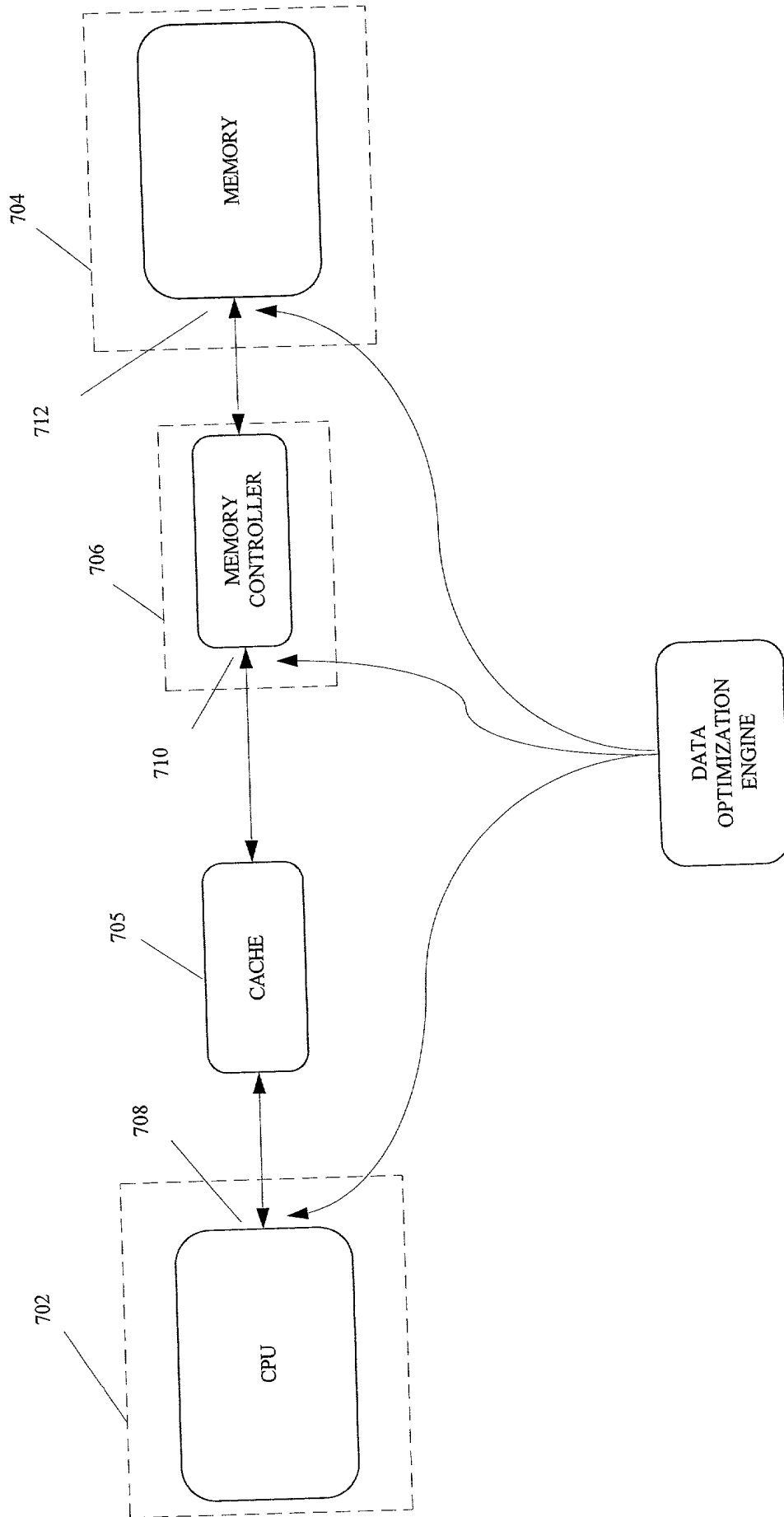


FIG. 6

FIG. 7



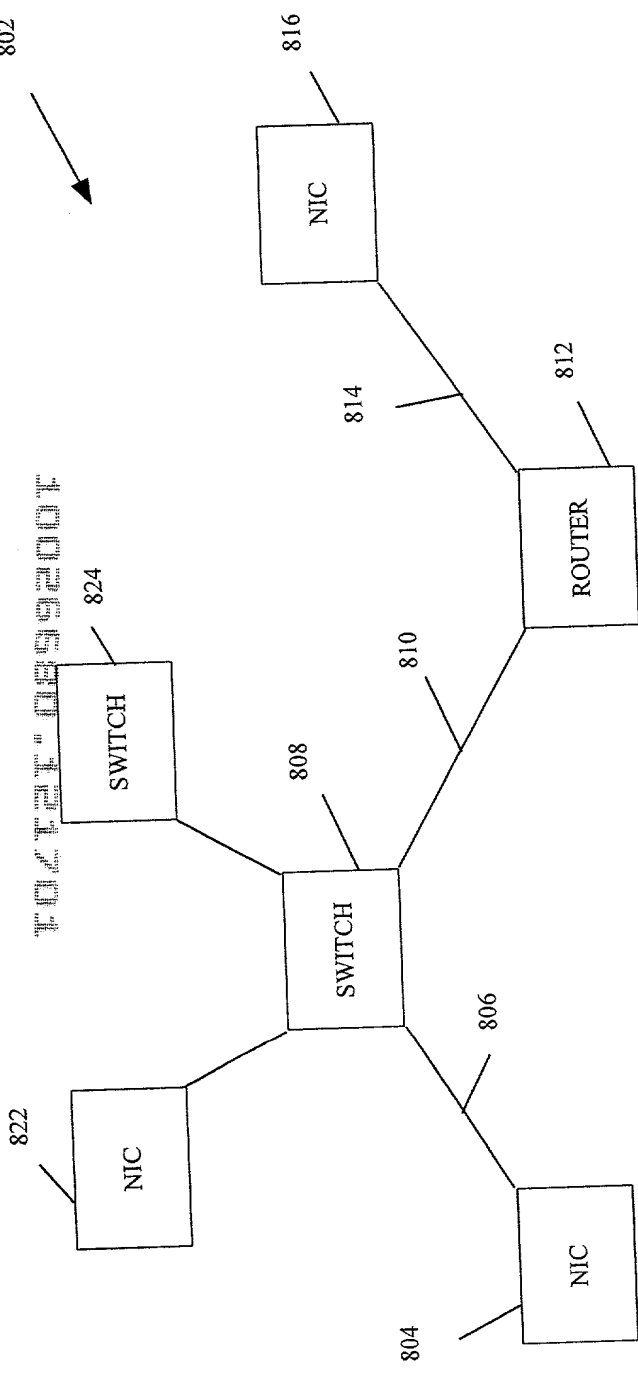


FIG. 8

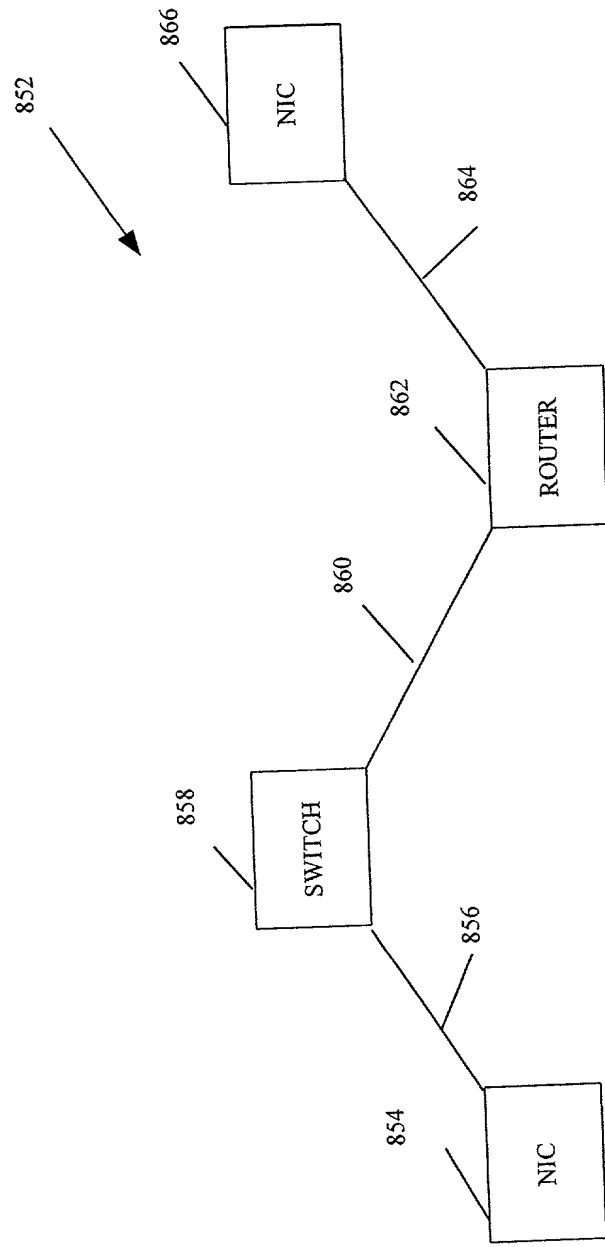


FIG. 9

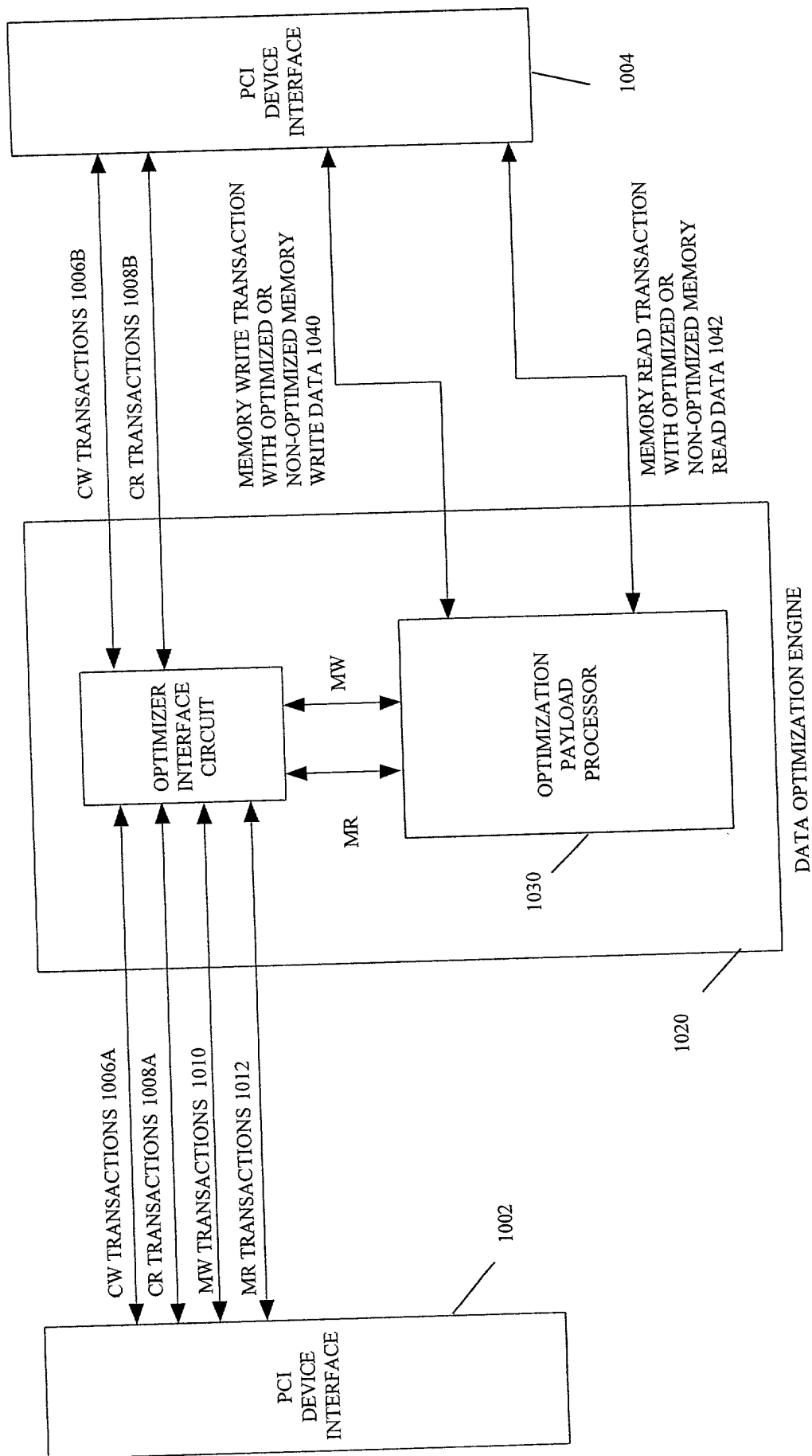


FIG. 10

INPUT DATA

FIGURE 11

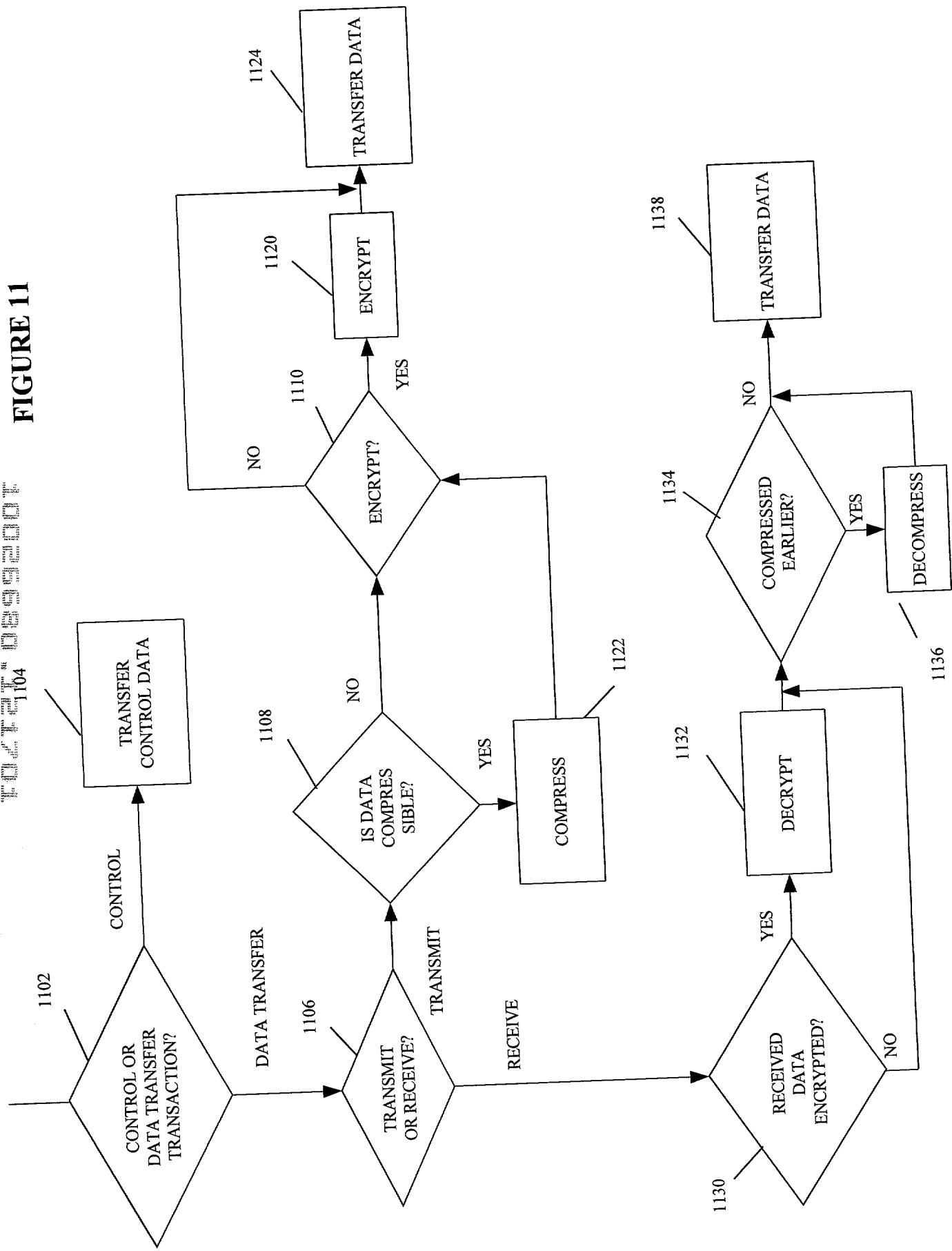
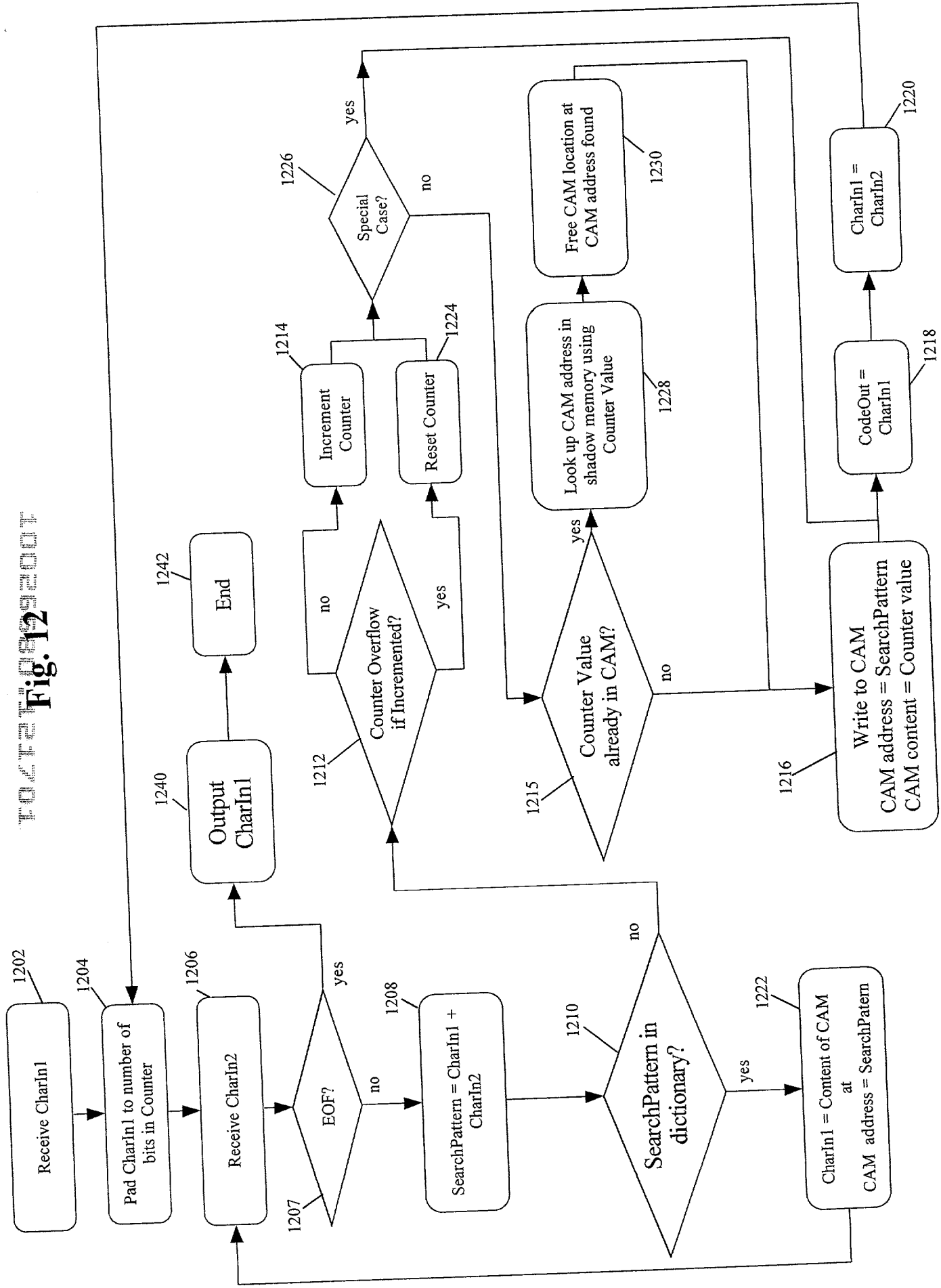
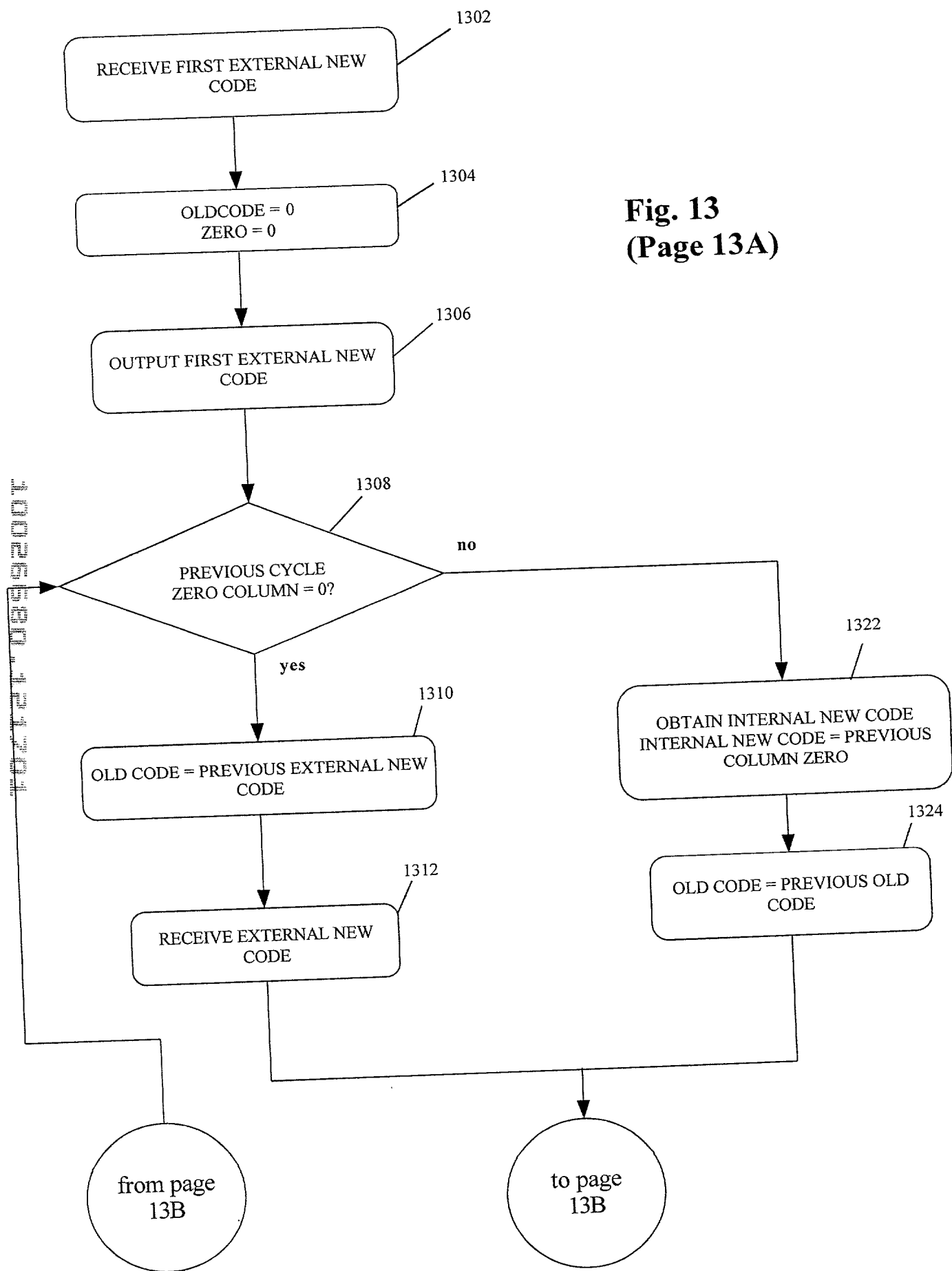


Fig. 12





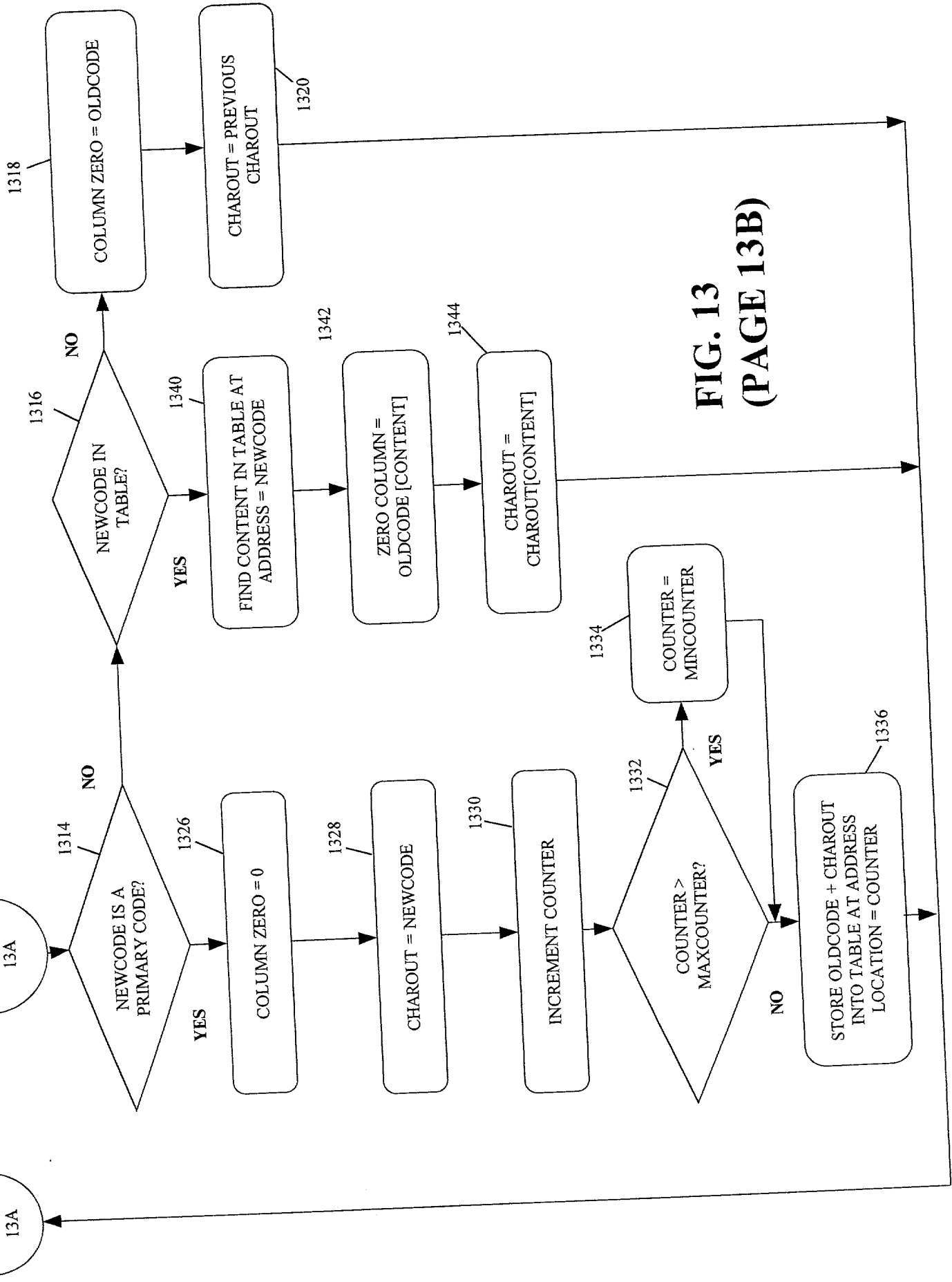
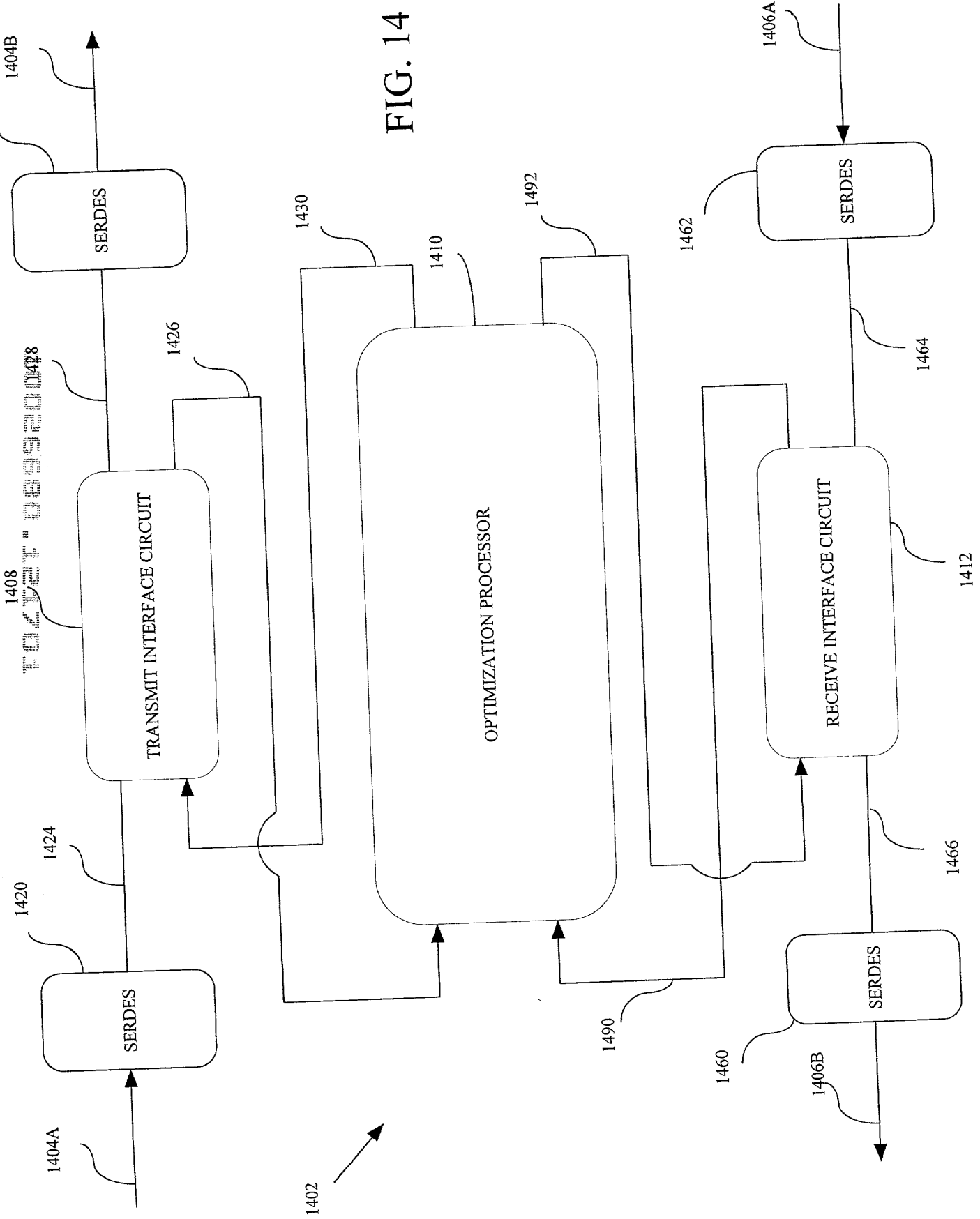


FIG. 13
(PAGE 13B)



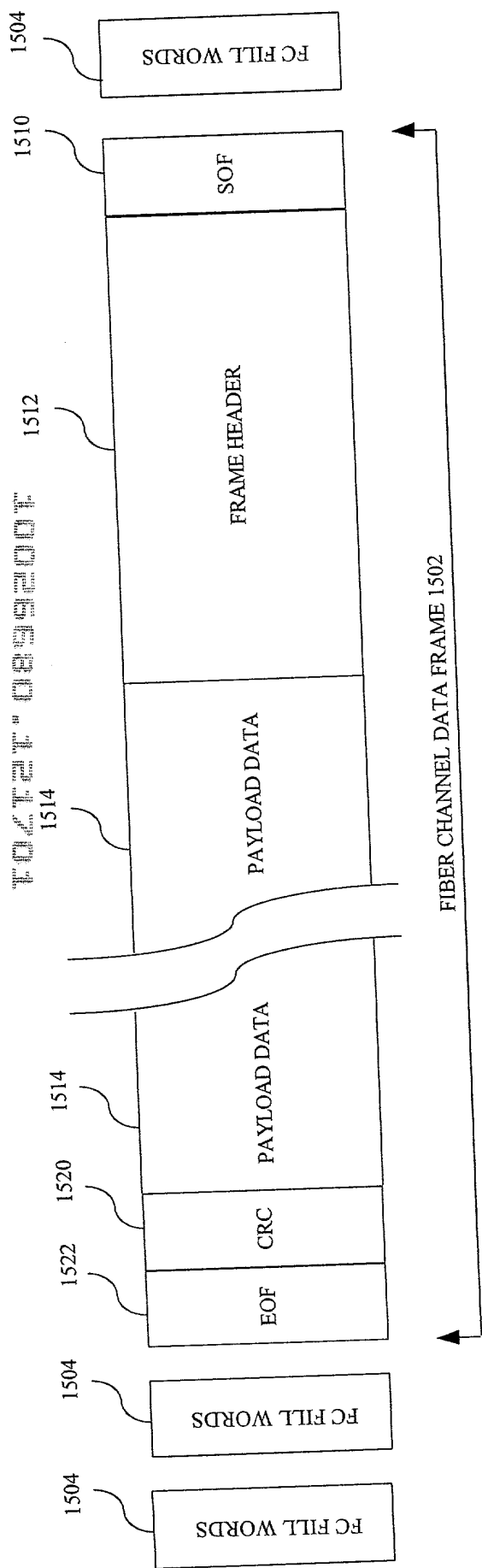


FIG. 15

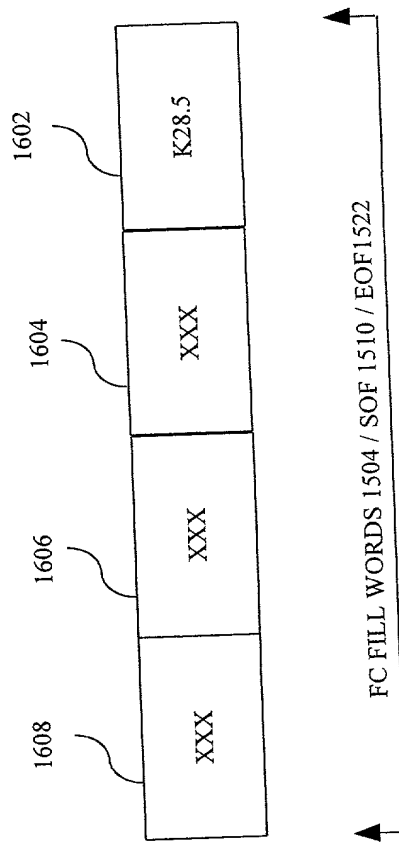
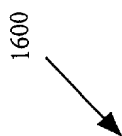


FIG. 16

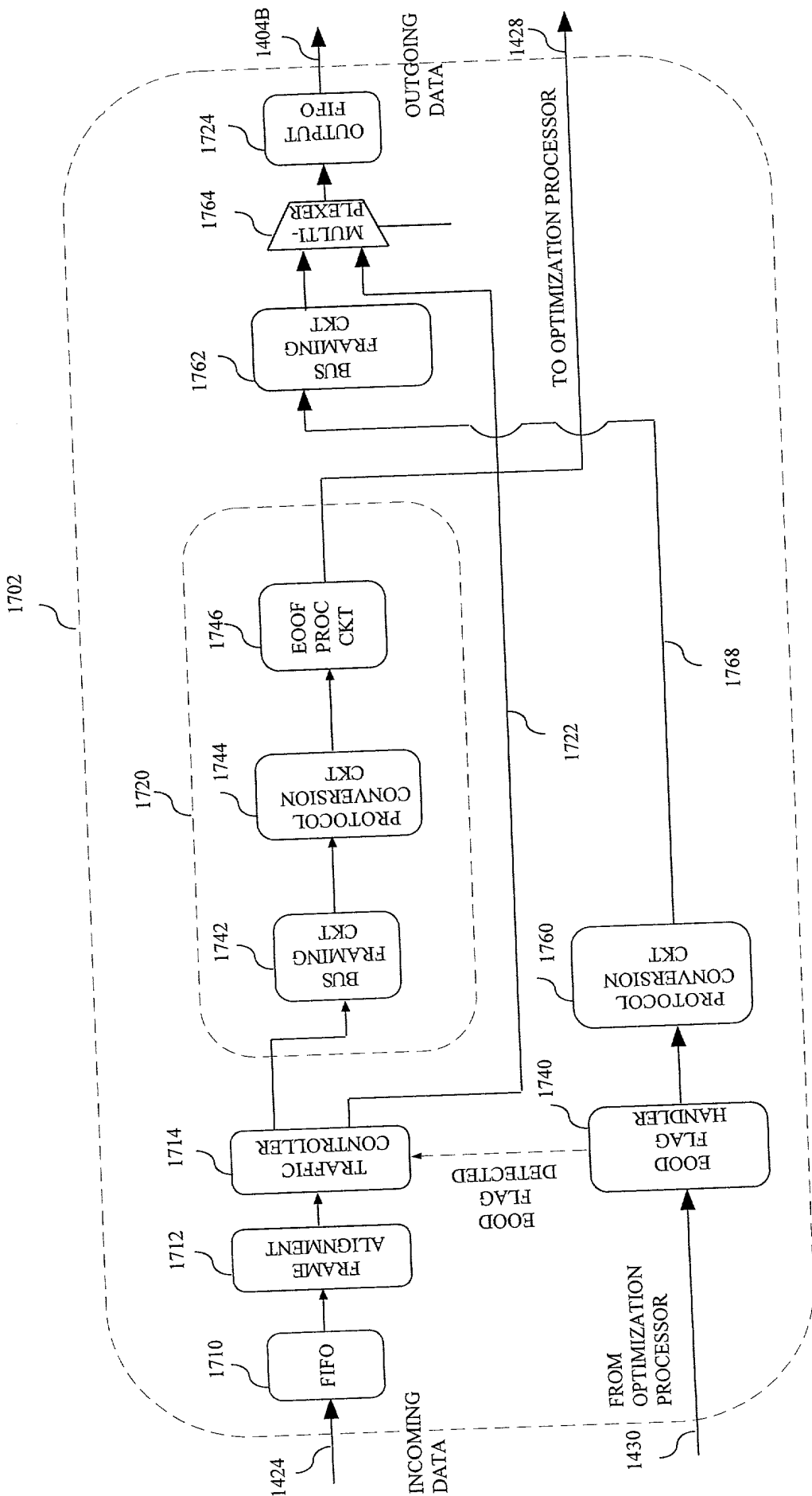


FIG. 17

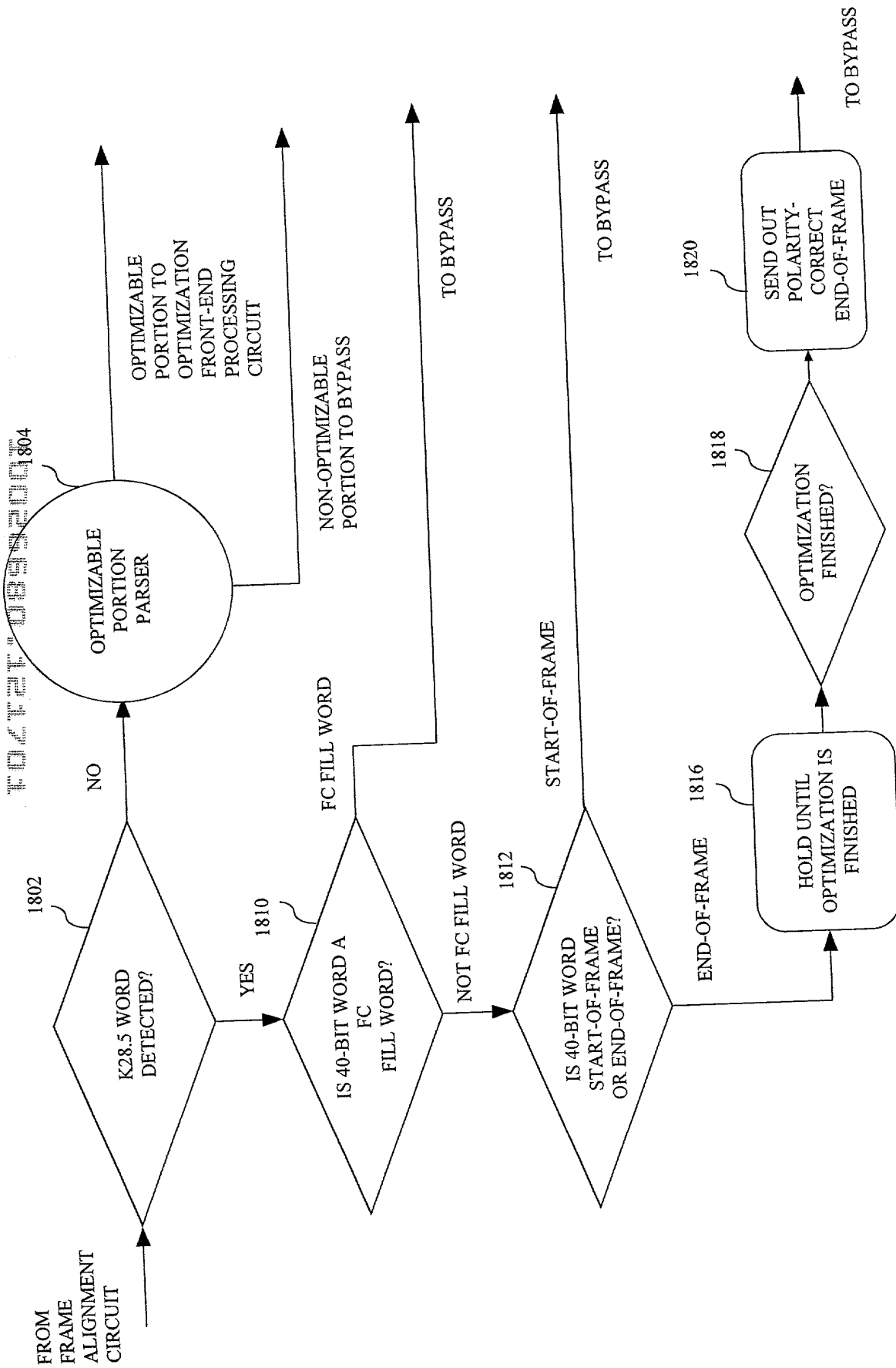


FIG. 18

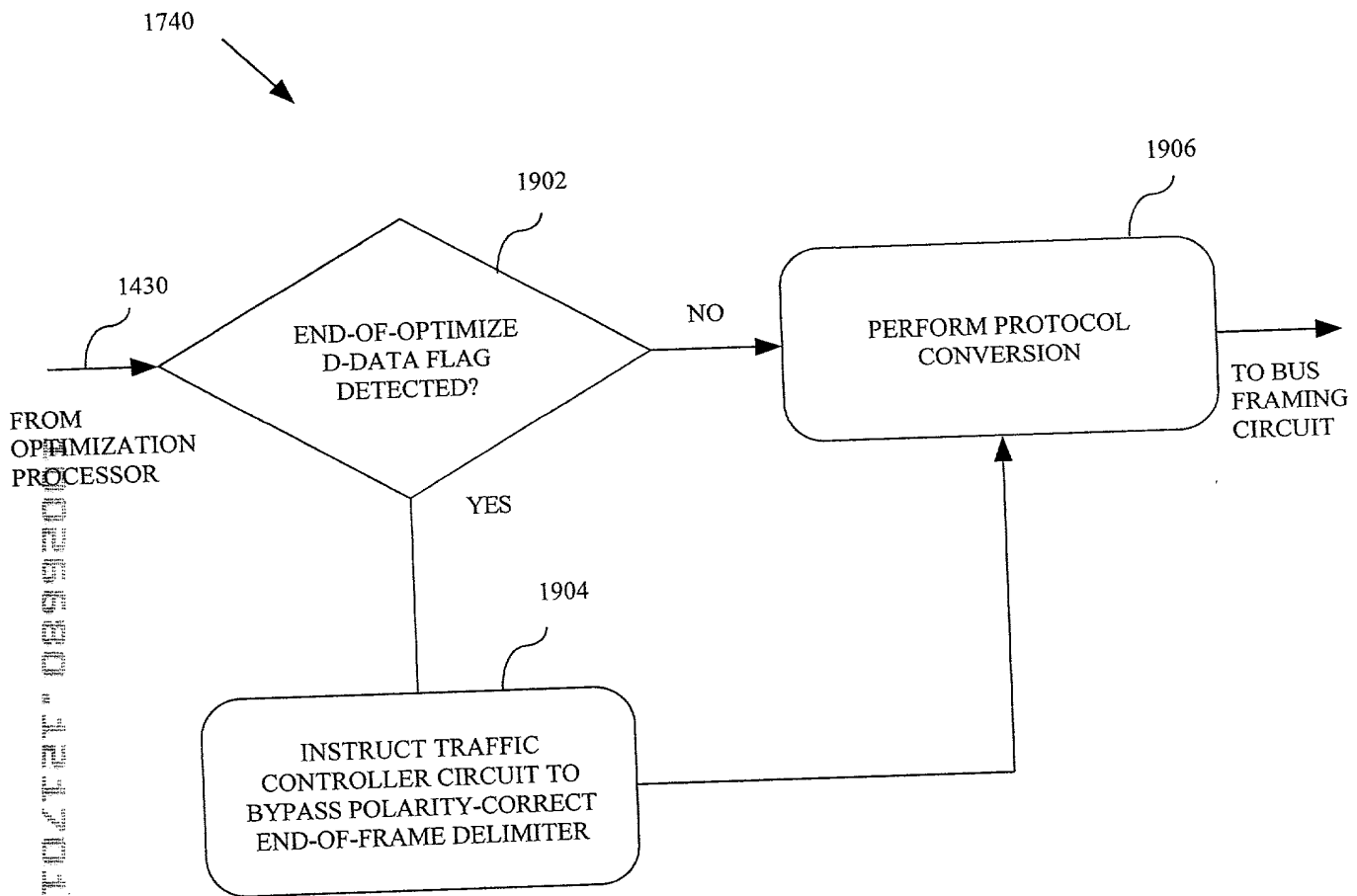


FIG. 19

FROM END-OF-OPTIMIZED-DATA
FLAG HANDLER CIRCUIT 1740

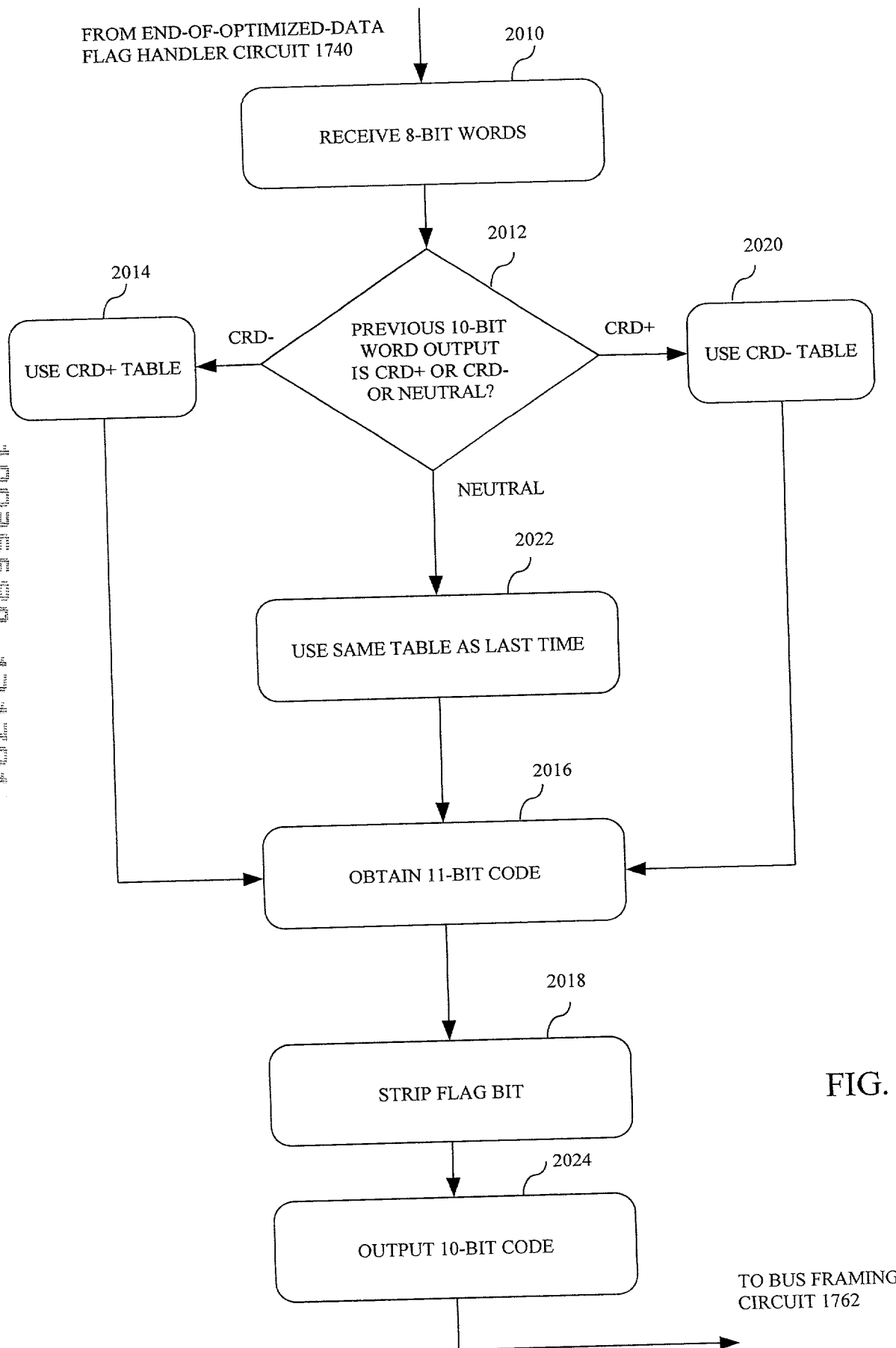


FIG. 20

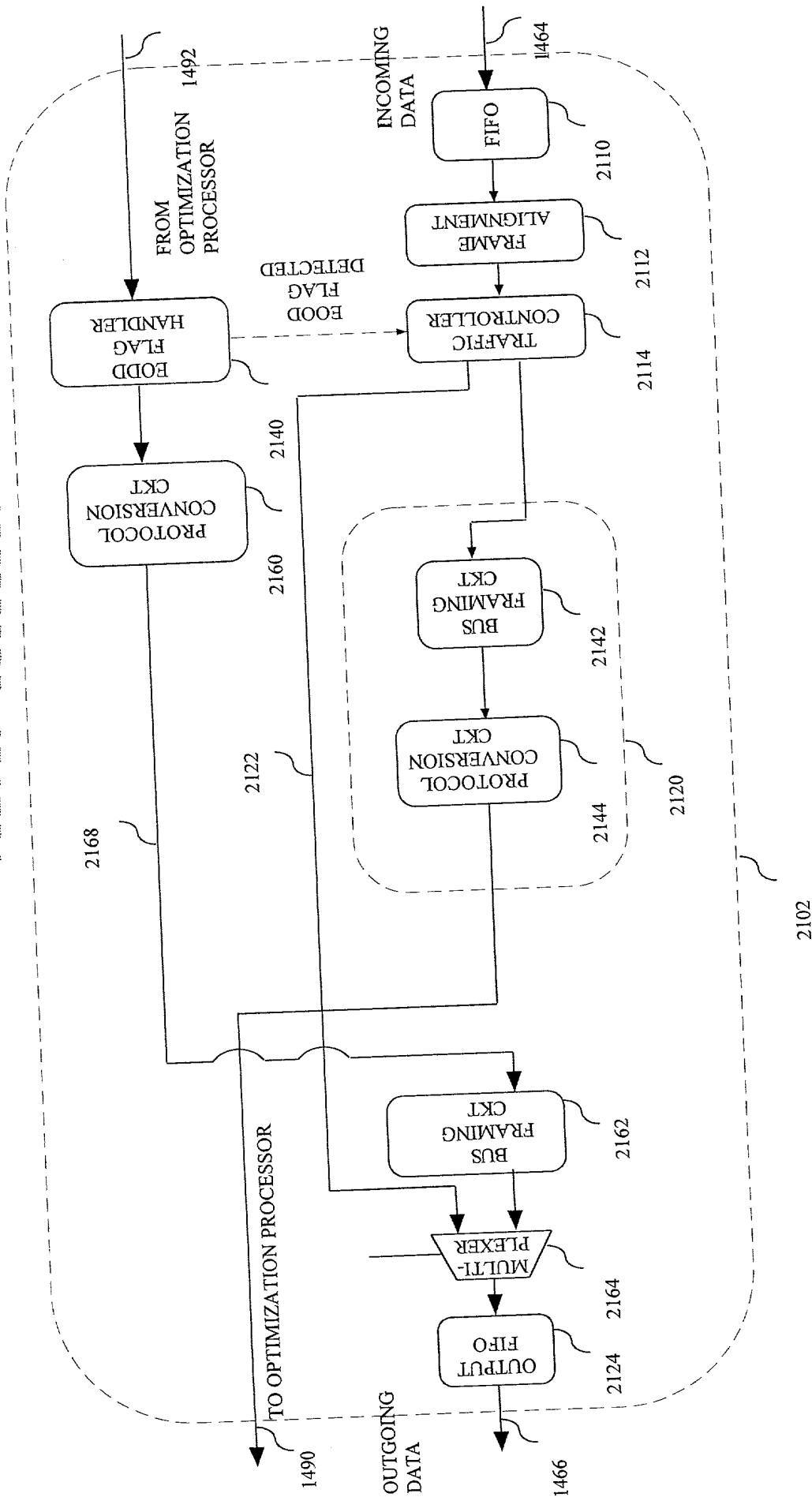


FIG. 21